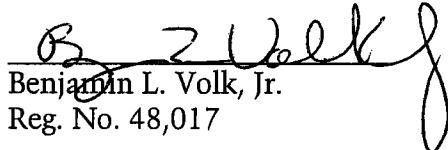


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF E-FILING**

I hereby certify that this correspondence has been e-filed with the U.S. Patent and Trademark Office on April 30, 2010.

  
\_\_\_\_\_  
Benjamin L. Volk, Jr.  
Reg. No. 48,017

In re application of: Chamberlain et al. :  
Serial No.: 10/550,323 : Examiner: Matthew Henning  
Filed: September 1, 2006 : Group Art Unit: 2431  
For: Intelligent Data Storage and Processing :  
Using FPGA Devices :  
:

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**ELECTION WITHOUT TRAVERSE IN RESPONSE TO RESTRICTION**  
**REQUIREMENT DATED MARCH 31, 2010**

In response to the Restriction Requirement dated March 31, 2010, Applicant files the following election.

**Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 8 of this paper.